

FIG. 1 is a block diagram of an image processing system 100. The system 100 includes an input device 4, a CPU 221, a timing generator 222, a rotary filter control mechanism 216, a white light source 211, an excitation light source 212, a mirror driving mechanism 234, a scan control circuit 235, a first-stage signal processing circuit 223, an OCT first-stage signal processing circuit 227, an OCT memory 228, an OCT picture processing circuit 229, a picture signal processing circuit 225, a video capture 226, an RGB memory 224, and a monitor 3. The system 100 also includes a first-stage signal processing circuit 223, an OCT first-stage signal processing circuit 227, an OCT memory 228, an OCT picture processing circuit 229, a picture signal processing circuit 225, a video capture 226, an RGB memory 224, and a monitor 3. The system 100 also includes a first-stage signal processing circuit 223, an OCT first-stage signal processing circuit 227, an OCT memory 228, an OCT picture processing circuit 229, a picture signal processing circuit 225, a video capture 226, an RGB memory 224, and a monitor 3.

FIG. 1

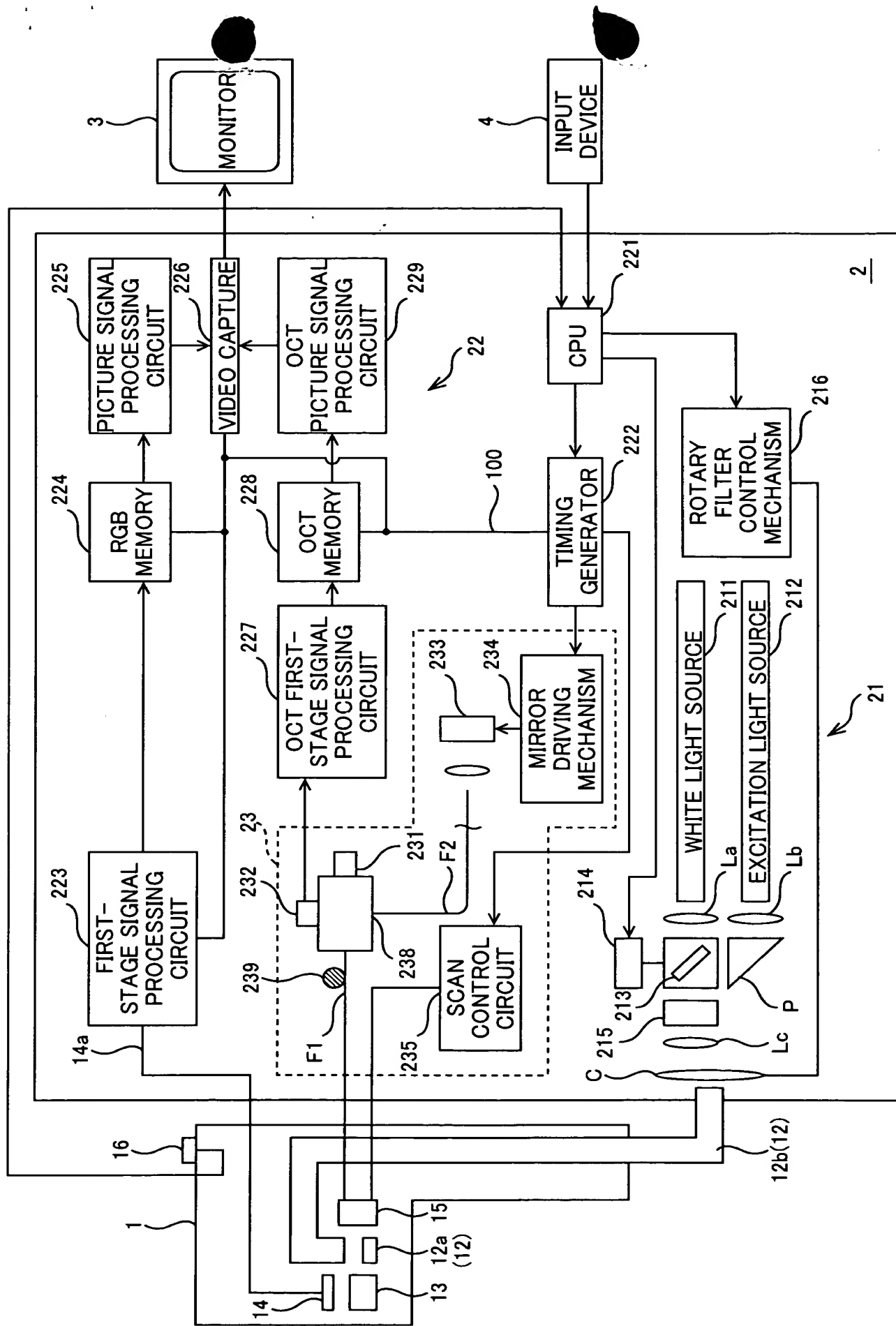


FIG.2

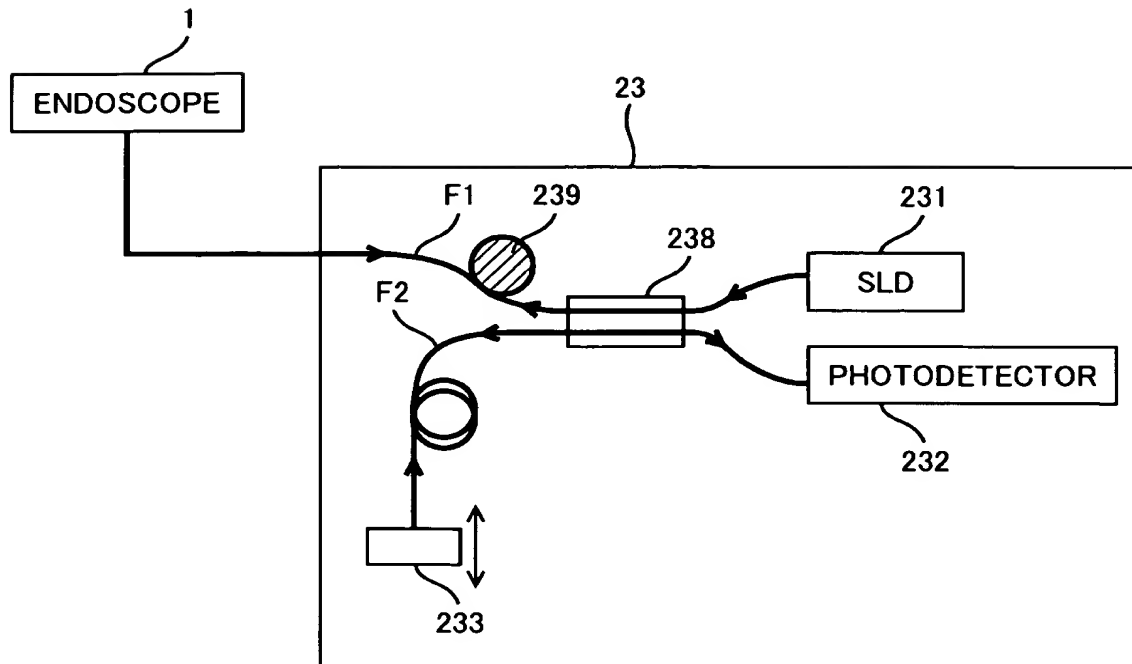


FIG.3

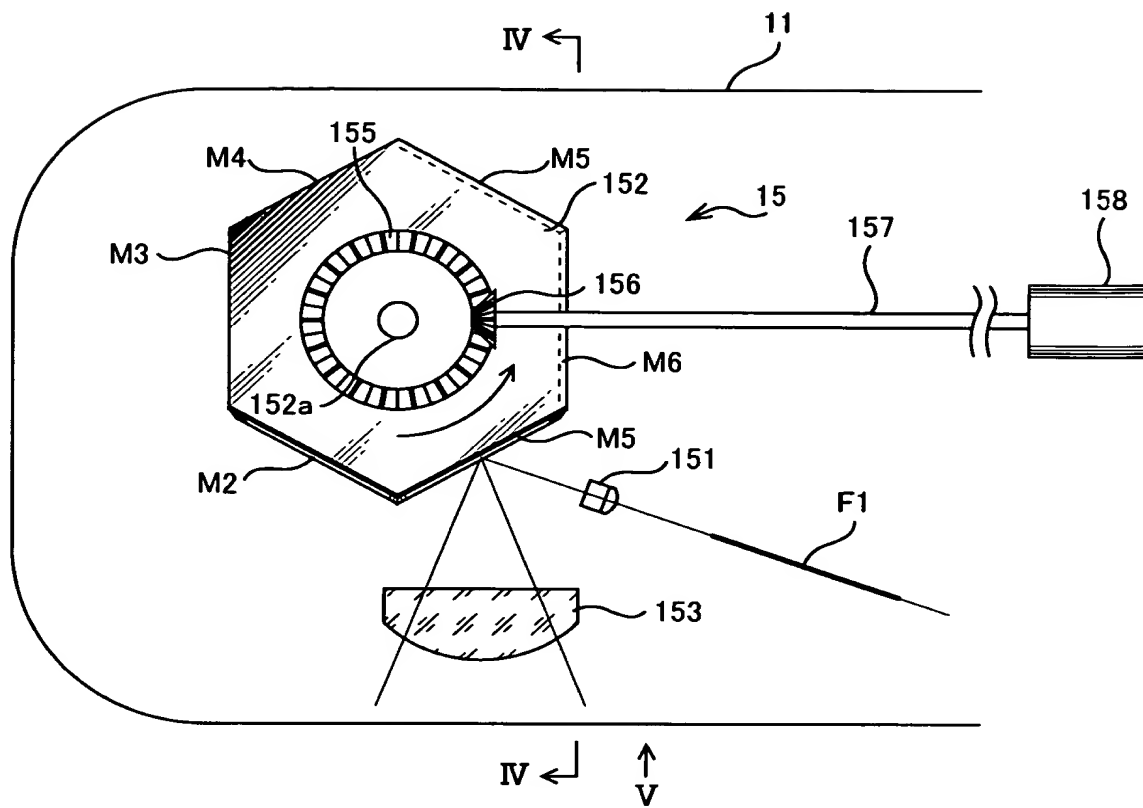


FIG.4

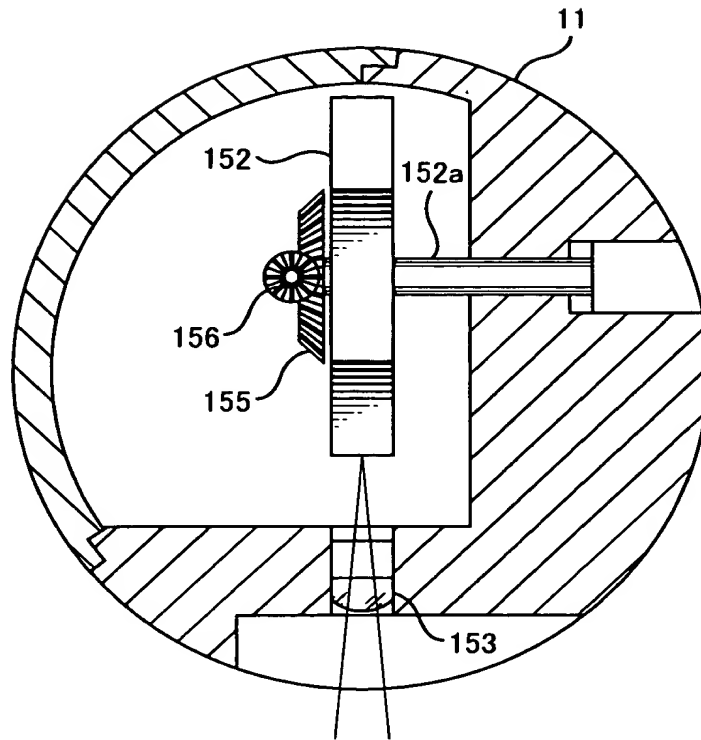


FIG.5

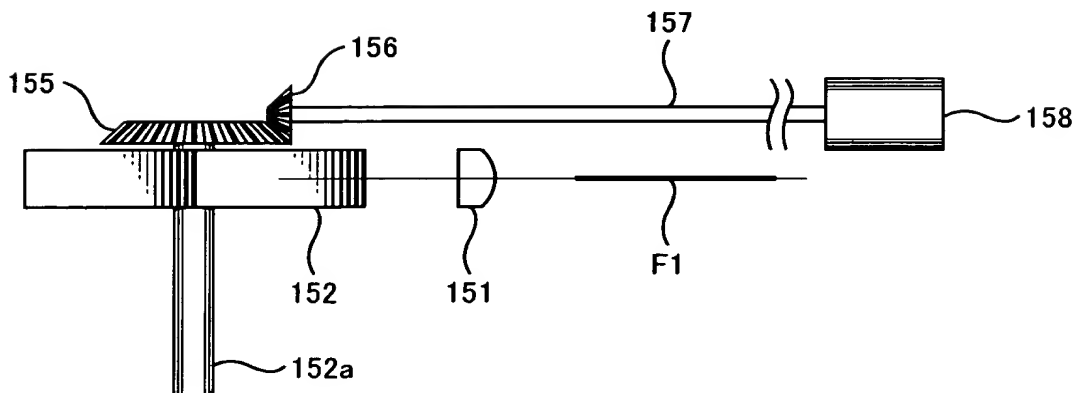


FIG.6

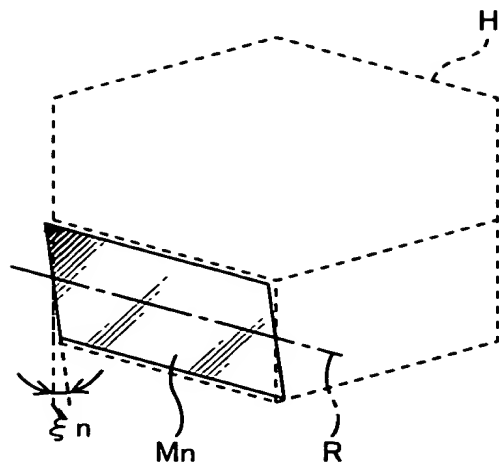


FIG.7

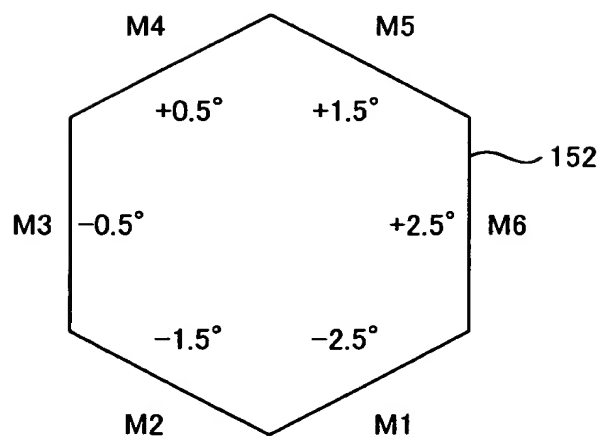


FIG.8

